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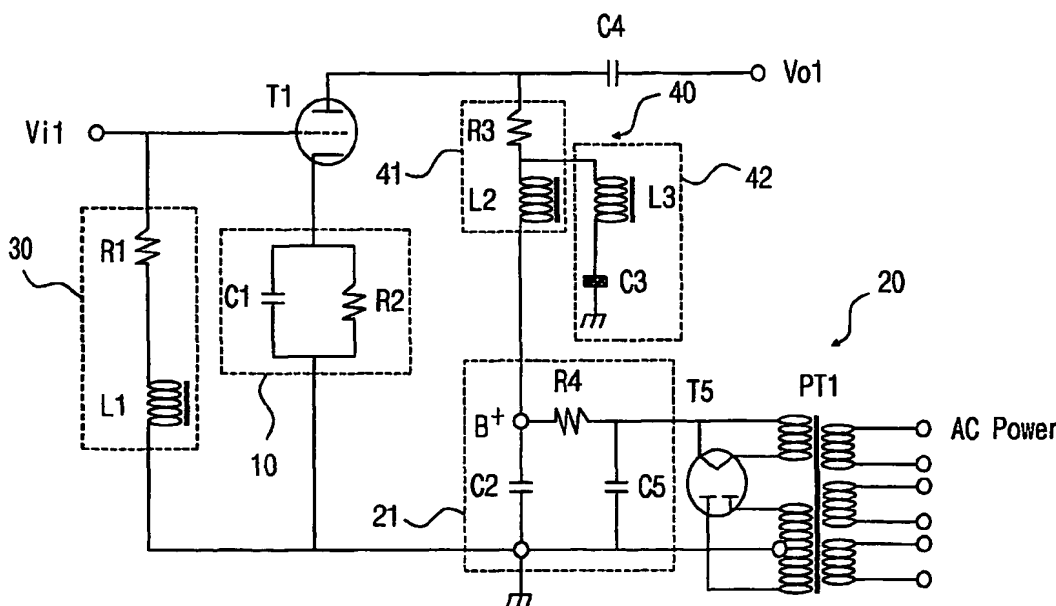
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(54) Title: INPUT/OUTPUT SIGNALS PRESERVER CIRCUIT OF AMPLIFICATION CIRCUITS



(57) Abstract: The present invention relates to an input and output signal preservation circuit of an amplification circuit capable of preventing an attenuation of an input signal and an output signal of an amplification circuit in such a manner that an AC input signal is amplified using an amplification device such as a vacuum tube or a transistor in a preamplifier.

WO 2005/031968 A1



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